



Low Noise, Low SWaP Synthesizers

Pronghorn Solutions
Denver, Colo.

RF/microwave synthesizers are ubiquitous in today's world — practically in every wireless device, from the smallest handheld to the largest communication towers, in every electronic countermeasure (ECM) and electronic warfare (EW) system, in medical and plasma research, in radio astronomy, in instrumentation and test, in laboratory and production environments.

Pronghorn Solutions has developed a new family of synthesizers, covering 10 MHz to above 24 GHz and offering industry-leading phase noise, frequency resolution of 0.01 Hz, fast switching of less than 100 μ s, low spurious, small size and low power consumption. The products in the PHS8500 family are available in modular (PHS8500M), handheld (PHS8500H) and benchtop (PHS8500B) formats. The benchtop model allows multiple synthesizers in one box, all controlled by a single reference source, with options for multiple tone testing and phase continuous, multiple frequency operation. The handheld and modular systems weigh under 1 lb and consume less than 8 W, with a power saver option for lowering power consumption in airborne units. The handheld unit offers battery operation as an option. The modular version is quite small: 3.5" \times 5.5" \times 1". With these three configurations, the PHS8500

family is suited for production and operational test, laboratory test and field applications.

INSIDE THE BOX

Phase-lock loop (PLL) synthesizers have been the workhorse in telecommunications, EW, ECM and test and instrument systems for well over 50 years. During this time, PLLs have evolved from complex, typically narrow band designs that were limited to below L-Band frequencies to integrated, multi-octave and multi-decade ICs. Digital processing speeds have also been increasing, enabling direct digital synthesizers (DDS) to reach well over 4 Giga symbols per second (GSPS). Direct analog synthesizers (DAS), which derive the desired frequency by combinations of multiplying and mixing a reference signal, have become smaller and achieve wider bandwidth, using ever-shrinking IC designs. The patented PHS8500 family uses a combination of the best characteristics of PLL, DDS and DAS technologies to create superior performance in a low power consumption, flexible package — a specific goal of the design team.

The PHS8500 operates from an internal 100 MHz temperature-compensated crystal oscillator (TCXO). It can also use an external 10, 100 or optional 1000 MHz reference. The

