THE PHS 8500 FAMILY OF VERY LOW PHASE NOISE HIGH PERFORMANCE MICROWAVE SYNTHESIZERS

BENCHTOP

MODULAR

HANDHELD
The PHS 8500 Family

Features:
- **Standard Range**: 700 MHz to 18 GHz
- **Extendable to**: 10 MHz to > 24 GHz
- **Phase Noise**: -110 dBc/Hz, 100Khz offset, at 24 GHz
- **Step sizes to**: .01 Hz
- **+5 dBm min at 24 GHz** with multiple higher power options
- **>10 dB power variation capability**
- **0 – 55 C temp range with -20 to +70 C option**
- **Modulation options (call factory)**
- **<100 μSec switching in List Mode**
- **Single Supply 7.5 V, < 1.5Amp**
- **Power Saver and Quick Start Modes**
- **100 MHz internal reference**
- **10/100 MHz external reference**
- **Ability to do “Specials” with lower Phase Noise in smaller ranges (e.g. octave)**
- **USB / SPI / LAN Interfaces**
- **IVI, SCPI, LabView® and MATLAB® compatibility. GPIB interface option**

The **8500 Family** is available in the Modular 8400M and the Benchtop 8400B Single Output and Multiple output formats. The Handheld version is available with Battery Option in certain Models. Please contact Pronghorn with your specific requirement.

**CUSTOM DESIGNS**

Pronghorn welcomes custom design requirements. We can add items such as a closed loop control of power, modulation capabilities, or different power supplies or mechanical configurations. Please refer to our Design Capabilities section on the web site and contact us.

**A BRIEF DESCRIPTION OF PHS MODELS**

Our synthesizer product families are the 8300, 8340, 8400, and 8500. The 8300 and the 8340 are Fractional – N Phase Locked Loop (PLL) models, and the 8400 and 8500 are Integer – N PLL models, which results in no so called fractional spurs. Our instruments are protected under US Patents 8611845, 8749282, and 9134355, and a number of other patents that are pending.

As can be seen from the Summary Parameters our instrument families represent a wide range of performance. In addition, each instrument is available in one or more of three models, the M for modular, with lowest power consumption, the B for Benchtop, which allows addition of higher power and closed loop leveling capabilities, and the H for Handheld, which allows for Field use, especially when equipped with the available optional battery.

**INTEGER PROGRAMMING**

The PHS 8500 M, B, and H are INTEGER SYNTHESIS based PLL Synthesizers. They do not exhibit any fractional spurious outputs such as Fractional-N based synthesizers do.

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**SPECIFICATIONS**

**Frequency Range**: 700 MHz – 18 GHz  
**Low End Options**: Specifiable  
**High End Options**: Specifiable  
**Frequency Resolution (Step Size)**: 1 KHz  
Options  
**Frequency Reference (Internal)**: 100 MHz, 10 ppm  
Options  
**Frequency Reference (External)**: 10 MHz Frequency @ 1 ppm  
External reference: 10 MHz / 100 MHz specifiable  
**Phase Noise and stability** are reference related  
**Frequency Switching Time in List Mode**: <100 µSecs  
**Frequency Sweep No of Points**: up to 1000  
**Frequency Sweep Dwell time**: 1 µSec to 1 Sec  
Continuous sweep and Optional Triggered sweep available  
**Power Output**:  
- < 15 GHz: >10 dBm  
- >15 GHz: > 5 dBm  
**Higher Power Output Options in some Models**:  
**Power Control Range**: > 10 dB  
**Power Control Step Size**: 1.0 dB  
**Power Level Accuracy**: ± 2 dB  
**Power Sweep Number of points**: 1 dB step size  
**Power Sweep Dwell Time**: 1 µSec to 1 Sec  
Continuous sweep and Optional Triggered sweep available  
**Operating Temperature Range**: 0 to 50°C  
Optional -20 to + 70°C range available  
**Storage Temperature Range**: -20 to + 80°C  

**Spectral Purity**  
**Phase Noise... See attached charts**  
**Spurious**: < 60 dBc Typical

The 8400 Series is Integer Programmed. There are no fractional spurious outputs.

**Programming/Interface Options**  
**All PHS models are available with a variety of interface options**, including Keypad Input via front panel in the B and H models, and  
**C# Based USB Programming with Pronghorn GUI in the M, B, and H models, and**  
**IVI and SCPI Compatible and compliant interface with**  
- LabVIEW®  
- MATLAB®  

**SPI and USB Interfaces**  
**LAN and GPIB add on options available**  

**Interfaces Available**  
**Modular – “M” Series**  
- USB  
- SPI  
- LAN / GPIB extension Options

**Benchtop – ”B” Series**  
- USB  
- SPI  
- LAN / GPIB options  
- Multiple unit Option (in one box)

**Handheld – “H” Series**  
- USB Interface  
- LAN / SPI Options

The 8400 “H” series is available only as a non-standard special at this time. Please contact your representative of PHS Office for availability, specifications, and pricing.
PHASE NOISE:

The PHS-8500 employs a new architecture that allows PLL performance with State of the Art Phase Noise. An actual measurement at 10 GHz is shown below. Commensurate phase noise will be available up to and beyond 24 GHz. The phase noise close to carrier can be improved by optional or externally provided frequency reference.
**DC Power Requirements**

The 8500 Series is powered by 7.5 V DC Voltage drawing <1.8 A of current. The B and H Series are supplied with 120 – 220 V AC to 8.4 V DC dongles. The Modular M series is powered by a pin in the SPI interface.

**Power Saver Mode and Quick Start**

The 8500 series have an option for a “Power Saver Mode” available. In this mode, all the circuits that are capable of quick recovery are turned off. The power dissipation in this mode is approximately 10% of the power dissipation under normal operation. The “Quick Start” feature allows the unit to be fully functional in milliseconds, and capable of its normal <100 microsecond switching performance.

**Multiple Unit Operation**

The 8500 B and 8500 M are designed to operate multiple synthesizers with an external reference oscillator. In this capacity, one can practically switch between frequencies below 1 GHz to above 30 GHz in a phase coherent fashion at extremely high speed. The 8400 B in particular is designed to control 5 - 6 synthesizers from a common interface, and provide a single combined output or multiple outputs based upon system needs. In such a system, phase coherent switching between multiple frequencies can occur at the speed of a microwave switch, typically microseconds. Furthermore, multiple 8500 B’s with a common reference frequency can be ganged together to provide 10 – 100 phase continuous and coherent outputs in a space that is a fraction of and at a cost that is a fraction of alternative solutions, with a speed that is superior to these alternative solutions. Please see Pronghorn AN – 6001, Phase coherent and phase continuous switching possibilities at our web site.

The PHS 8500 series is also very useful in applications that need multi tone testing, especially when coupled with our closed loop power control add on units. The Multi Tone frequencies in our applications are not limited to a 90 or 120 MHz bandwidth as in the case of so called Vector Signal Generators. An example of a B model is shown in this data sheet.

**SUPPORTING DOCUMENTATION**

Our web site contains all the relevant documentation relating to the sizes, mounting configurations, and recommendtions relating to the M, B, and H series. In addition, Operating, USB and SPI programming manuals and details are available on the web in the documentation section.

A variety of Application Notes and Comparison Tables are also available.
“M” MODELS CONNECTIONS (Single Sided Inputs and Outputs)

Size : 3.5” X 5.5” X 1.0”

SPI Connections – M and B Models

<table>
<thead>
<tr>
<th>Pin No.</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 – 5,8</td>
<td>N/C</td>
</tr>
<tr>
<td>6</td>
<td>Lock Indicator</td>
</tr>
<tr>
<td>7</td>
<td>Pulse / Trigger (Option)</td>
</tr>
<tr>
<td>20</td>
<td>SPI SCLK</td>
</tr>
<tr>
<td>18</td>
<td>SPI SS</td>
</tr>
<tr>
<td>16</td>
<td>SPI SDI – Data Input</td>
</tr>
<tr>
<td>14</td>
<td>SPI SDO – Data Output</td>
</tr>
<tr>
<td>13, 15</td>
<td>RX/TX option. Normally N/C</td>
</tr>
<tr>
<td>17</td>
<td>SWP Trigger Option</td>
</tr>
<tr>
<td>19</td>
<td>Control Option (e.g. List Trigger)</td>
</tr>
<tr>
<td>11,12</td>
<td>8V DC In (M models)</td>
</tr>
<tr>
<td>9,10</td>
<td>Ground (M Models)</td>
</tr>
</tbody>
</table>
Detailed M, B, and H package drawings can be found in the Documentation section of the web site.